All communications respecting this case should identify it by number and names of parties.



UNITED STATES DEPARTMENT OF COMMERCE Patent and Trademark Office

Address: BOX INTERFERENCE

Commissioner of Patents and Trademarks

Washington, D.C. 20231

Telephone: (703) 308-9797 Facsimile: (703) 308-7953

MAILED

MAR 1 8 1996

PAT.&T.M. OFFICE BOARD OF PATENT APPEALS AND INTERFERENCES Applicants: Evans Jr. et al. Serial No.: 07/582,672

Filed: 09/14/90

For: Non-Volatile Memory Circuit Using Ferroelectric Capacitor Storage Element Accorded benefit of U.S. Serial No. 07/440,434 filed 10/20/89 and 07/057,100 filed 06/02/87, now abandoned

Pursuant to the Decision on Preliminary Motions of November 2, 1992, Interference No. 102,725 is redeclared by designating claims 61, 62, 64, 65, 67, 68 and 70-72 of Evans, Jr. et al., claims 1-6 of Schwee and claims 2-5 and 18-22 of Eaton, Jr. as corresponding to count 1.

The claims of the parties which correspond to count 1 are as follows:

Evans, Jr. et al.: $\begin{cases} claims 61, 62, 64, 65, 67, 68 \text{ and} \\ 70-74 \end{cases}$

Schwee

: claims 1-7 and 9

Eaton, Jr.

claims 1-5 and 17-22

Stanley M. Trynowiez, fr. Administrative Patent Judge

(703) 308-9772

gjh